

**One Week**  
**Entrepreneurship and Skill**  
**Development Training Programme**



**MSME**  
MICRO, SMALL & MEDIUM ENTERPRISES  
सूक्ष्म, लघु एवं मध्यम उद्यम  
OUR STRENGTH • हमारी शक्ति

Ministry of MSME, Govt. of India

**Organized by**  
**Dept. of Electronics and**  
**Communication Engineering**  
**National Institute of**  
**Technology Sikkim**

**Ravangla, South Sikkim, India, -737139**

**Website: [www.nitsikkim.ac.in](http://www.nitsikkim.ac.in)**

Under the Sponsorship of  
**Ministry of MSME Govt. of**  
**India**

## About NIT Sikkim

National Institute of Technology Sikkim, an institute of national importance is one among the ten newly sanctioned NIT(s) by the Government of India in 2009. The institute offers B. Tech programs in Computer Science and Engineering, Electronics and Communication Engineering, Electrical and Electronics Engineering, Mechanical Engineering, and Civil Engineering. Moreover, the institute offers M.Tech programs in VLSI and Embedded Systems, Communication and Signal Processing, Electrical and Electronics Engineering, and AI and ML. The Institute also offers M Sc. program in Chemistry and Ph. D programs in all departments.

Presently, NIT Sikkim is located in a temporary campus at Ravangla, in South Sikkim which is a tourist town and it is connected by highway to other major towns in the state and lies between Pelling and Gangtok. Ravangla is situated at an elevation of 2100 m surrounded by Himalayan terrain and famous for tourist spots such as Buddha Park, Temi tea garden, and Ralong Monastery.

## About the Dept. of ECE

The Dept. of ECE was established in 2010 with the laying of the foundation of National Institute of Technology Sikkim. The Department aims to provide an outstanding research environment complemented by excellence in teaching to produce engineering professionals leading a successful career in industry, academics, and entrepreneurial endeavors. The Department offers research in the areas of ASIC & Modeling and Optimization of High-Performance Semiconductor Devices, Microwave Engineering & Antenna Design, Wireless Communication, Speech Processing, Satellite Communication and Navigation. The Dept. has a good laboratory facility with modern equipment to encourage the students to cope up with the latest technologies. The Department offers B. Tech in ECE with an intake of 30 students/ year, M. Tech with an intake of 22 students/year and Ph. D in Engineering.

## About workshop

Industries required skilled manpower in the field of VLSI design. It follows that there is a need to develop manpower that has expertise not only in VLSI design but also in System on Chip/ System Development.

- This training program will provide an opportunity to the students/entrepreneurs to develop a project oriented, hand-on training experience in the field of Nano-scale VLSI, as per industry requirements. This will help them in fulfilling the aim towards the design of products and services for the society and MSME sectors.
- The students/entrepreneurs should be able to understand the importance and prospect of the Nano-scale VLSI integrated circuits.
- The entrepreneurs should learn the advanced VLSI design technique using various EDA tools by hands on experiments'

## Brief description of the topic:

In the past two decades, CMOS technology has rapidly embraced the field of integrated circuits, providing low cost, high performance solutions and rising to dominate the market. The size of the device is gradually decreasing towards nano scale. While silicon bipolar and III-V devices still find niche applications, only CMOS processes have emerged as a viable choice for the integration of today's complex mixed signal and SOC systems. Both the Digital and Analog IC design have evolved with the technology moving from high voltage, high power small scale to low voltage, low power ultra-large-scale integrations.

This training programme will focus on the Future Roadmap of Electronics Industries. The participants will be able to learn the VLSI design flow with hands-on experiences using EDA Tools. In this workshop it will focus on Analog IC design, Mixed signal RFIC design, Digital IC design and complete PLL design flow.

## Resource Persons

Eminent experts from Industry and Academia (IITs, NITs, IIITs, etc.) will be delivering talks along with hands-on sessions.

## Organizing Committee

### Patron

**Prof. M. C. Govil**  
Director, NIT Sikkim

### Convener

**Dr. Sanjay Kumar Jana**  
Head of Department, Dept. of ECE, NIT Sikkim

### Coordinator

**Dr. Hemant Kumar Kathania**  
Assistant Professor, Dept. of ECE, NIT Sikkim

## Program Committee

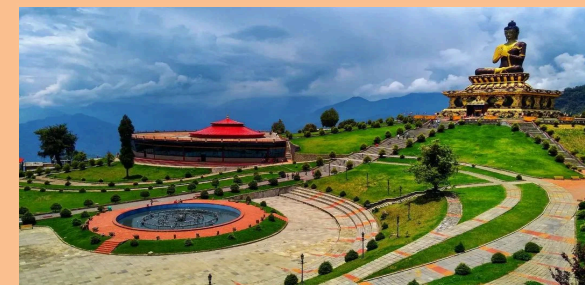
**Dr. Prataya Kuila**, Dean R&C  
**Dr. Jeetendra Singh**, Asst. Prof., Department of ECE  
**Dr. Neha Kailash Nawandar**, Asst. Prof., Department of ECE  
**Dr. Vishal Visnoi**, Assistant Professor, Dept. of ECE  
**Dr. Sukanta Dhar**, Asst. Prof., Department of ECE  
**Dr. Sudipta Das**, Assistant Professor, Dept. of ECE  
**Dr. Neelima**, Asst. Prof., Department of ECE



## Participants

Targeted participants are Entrepreneurs, Graduates, Post Graduates, who are looking for high end entrepreneurship in the sunrise sector. The workshop is also open for Faculty members of academic institutions and working professionals, scientists/ researchers from MSME and Industries. Other Important Information for Participants:

- The workshop will be conducted in physical mode.
- Seats are limited and the participants will be selected based on first come first serve. However, the preference should be given to the participants from the category of SC, ST, OBC, Women, PH and Minority.
- Shared accommodation and food for participants will be provided by NIT Sikkim. One Week Advanced Entrepreneurship and Skill Development Programme (A-ESDP) on Smart Healthcare Systems using IoT and AI/ML Funded by Ministry of MSME, Government of India 27th february to 02nd March 2024
- NIT Sikkim, Ravangla is situated at an altitude of 2100 meter and the weather remains cold throughout the year. So, it is requested from participants to carry warm clothes along with them.
- TA will be reimbursed to the participants as per the shortest railway/road routes (2AC/3AC) or equivalent fare upon submission of original tickets.
- The candidate must upload a No Objection Certificate (NOC) from the Supervisor/ Head of the Department/ Head of the Institute in the registration link for participating in the workshop.



## Registration

- There are no registration fees to attend the training program; however, registration is compulsory for participation.
- Seats are limited, shortlisted candidates will be called to attend the training program.
- Last date of registration is **25.02.2024**.
- Click the below link for registration:

[Click Here](#)



## Certificate

Certificates will be provided to the attendees after confirming the attendance criteria.

## Contact Persons

**Dr. Sanjay Kumar Jana** (voice: +91-9647658649, email: [skjnit@nitsikkim.ac.in](mailto:skjnit@nitsikkim.ac.in) )

**Dr. Hemant Kathania** (voice: 9732900589, email: [hemant.ece@nitsikkim.ac.in](mailto:hemant.ece@nitsikkim.ac.in))