

Result of backlog/supplementary (Reduced Grade)

Sl No	Roll No	Name of Student	Computational Mathematics	Network Analysis and Synthesis	Signals and Systems	Semiconductor Devices	Probability Theory and Stochastic Processes	Data Structure and Algorithms	Data structure and algorithm Laboratory	Network Analysis and Synthesis Laboratory	Signals and Systems Laboratory	Semiconductor Devices Laboratory	Professional Practice II
1	B180070EC	Kalagara varun chandhu	4	3	3	3	3	3	1	1	1	1	ZZ13201
2	B190054EC	Kommalapudi Anusha	DD	BD	DE	CD	DD						
3	B180078EC	ADHITIYA SHARMA	FA							CC	FP		
4	B170152EC	PANKAJ KUMAR											P
5	B190052EC	GOVARDHAN REDDY KOPPULA								FA			P

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