

## General Information

### About the Institute:

National Institute of Technology Sikkim (NITSKM) is one among the ten newly sanctioned NIT(s) by the Government of India in 2009 under the *Eleventh Five Year Plan*.

**Vision:** India has the capability and the responsibility to offer the World Science & Technology essentially with sustainability, through philosophy, conscience and value system. NIT Sikkim will play its role.

**Mission:** To develop the students as 'Thinking Engineers' by nurturing them in attaining and enjoying the technical and scientific excellence, global exposure and at the same time in beholding the philosophy and the values for India and the world as a whole.

### Courses Currently Offered

M. Tech in Microelectronics & VLSI Technology  
M. Tech in Computer Science and Engineering  
B. Tech. in Computer Science and Engineering  
B. Tech. in Electronics and Communication Engineering  
B. Tech. in Electrical and Electronics Engineering  
B..Tech in Civil Engineering  
B. Tech in Mechanical Engineering  
B. Tech in Biotechnology

### Location:

Presently, the institute is located at Ravangla, Barfung Block in southern region of Sikkim.

### Connectivity:

The New Jalpaiguri (NJP) Railway Station and Bagdogra Airport are 113 and 106 km away from the Campus respectively. Participants may reserve car from NJP or may reach by shared car and bus from SNT stand, Siliguri.

### Eligibility for Participation:

Research personnels in R&D organizations/Scientific officers and engineers working in cutting-edge technology from industries/Faculties of educational institutes engaged in teaching / research are eligible to participate. Ph.D, M.Tech and B.Tech students are also eligible to attend the course.

### Accommodation & food:

On prior intimation we will try to arrange accommodation to the nearby hotels with moderate charges that participants need to pay. Course fees includes lunch & tea-snacks.

### Registration fee:

Student : Rs. 500/-  
Faculty Member : Rs. 2000/-  
Industry Participant : Rs. 5000/-

### How to apply:

Interested persons may apply in the form given herewith along with the registration fee in the form of A/C Payee demand draft drawn in favour of "Director, NIT Sikkim", payable at Ravangla.

The application should be sent to the following address latest by September 26<sup>th</sup>, 2016. Limited seats will be booked as first come first serve basis.

### Mailing address:

Dr. Sanjay Kr. Jana  
Department of Electronics & Communication Engineering  
National Institute of Technology Sikkim  
Ravangla Campus, Barfung Block,  
South Sikkim 737139

Candidates may deposit the course fees in the account of "Director, NIT Sikkim" (A/C No. 31996269106) and mail the Application form and scanned copy of money deposit to [nitsikkimwmsrcd2016@gmail.com](mailto:nitsikkimwmsrcd2016@gmail.com) by September 26<sup>th</sup>, 2016..

### Contact Person:

Dr. Sanjay Kr. Jana  
FIC, ECE, NIT Sikkim  
Mob: +91-9647658649  
Email: [skjiit@gmail.com](mailto:skjiit@gmail.com)

Mr. Surajit Kundu  
Asst. Prof. ECE, NIT Sikkim  
Mob: +91-9832271039  
Email: [surajit.kundu@nitsikkim.ac.in](mailto:surajit.kundu@nitsikkim.ac.in)

## Workshop On "Mixed Signal and RF Circuit Design"

30<sup>th</sup> September – 01<sup>st</sup> October, 2016

Organized by



Department of ECE  
National Institute of Technology Sikkim

Ravangla Campus, Barfung Block,  
South Sikkim – 737139, India



## Importance & Scope of Workshop:

Exposure to analog and digital circuit design techniques in integrated context

Learn to design mixed-signal building blocks including comparators and data converters

Gain experience with system level design flow: bottom-up and top-down design methodologies.

Gain mixed-signal design experience in Cadence CAD tools, including both custom and automated design

Analog, Digital and Mixed-Mode simulation

Digital synthesis and Place & Route

Layout, DRC, LVS, Post-layout verifications

High-performance simulation of linear and non-linear RF circuits

Robust RF circuit verification

Verification of the RF design in context of on- and off-chip passive circuitry.

Familiarize students with methods for the analysis and design of circuits at RF

Investigate some topical aspects of RF signal design

Develop an appreciation of the complexity involved in RF networks

Provide an insight into the behavior of basic circuit devices at RF

Gain practical skills in software design tools

Develop an understanding of the building blocks of a wireless telecommunications system

## Organizing Committee:

### Chairman:

Prof. A. B. Samaddar  
Director, NIT Sikkim

### Workshop Coordinator:

Dr. Sanjay Kr. Jana  
FIC, ECE, NIT Sikkim

### Workshop Co-coordinator:

Mr. Surajit Kundu  
Asst. Professor, ECE, NIT Sikkim

### Organizing Committee Members:

Dr. W Ahmad  
Faculty, ECE, NIT Sikkim  
Dr. M Bera  
Project Faculty, ECE, NIT Sikkim  
Mr. H K Kathania  
Asst. Professor, ECE, NIT Sikkim  
Ms. R Dhara  
Asst. Professor, ECE, NIT Sikkim

## Experts:

Experts from IITs



## Registration Form:

### WorkshopOn “Mixed Signal and RF Circuit Design” 30<sup>th</sup> September – 01<sup>st</sup> October, 2016

Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Sex (M/F): \_\_\_\_\_

Highest academic Qualification: \_\_\_\_\_

Organisation: \_\_\_\_\_

Address: \_\_\_\_\_  
\_\_\_\_\_

Mob: \_\_\_\_\_

Email : \_\_\_\_\_

Details of bank draft: Amount (Rs): \_\_\_\_\_

Draft No. \_\_\_\_\_ Dated: \_\_\_\_\_

Issuing Bank: \_\_\_\_\_

Date: \_\_\_\_\_

Signature

Place:  
Recommendation and forwarding from the  
Organization:

Signature with seal of the  
Head of the Organization