

NATIONAL INSTITUTE OF TECHNOLOGY SIKKIM

Department of Electrical and Electronics Engineering

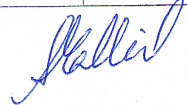
Date: 15th May, 2022

Admission to PhD Programme

Odd Semester, Academic Year: 2022-23

Advertisement No.: NITSK / Admission / 2022-23 / Odd Sem. / PhD. Date: 1st April, 2022

Application No.	Name of the Applicant	Category	Full-Time /Part-Time	Remarks
EEE/01	SOURAV DUTTA	UR	Full-Time	Provisionally Shortlisted
EEE/02	RAJESH KUMAR	SC	Full-Time	Provisionally Shortlisted
EEE/03	G PRASHANT	UR	Full-Time	<i>Not Shortlisted</i>
EEE/04	KOSHA KRISHNA DUTTA	UR	Full-Time	Provisionally Shortlisted
EEE/05	BISWAPRATIM ROY	UR	Full-Time	Provisionally Shortlisted
EEE/06	SHASHI KUMAR	SC	Full-Time	<i>Not Shortlisted</i>
EEE/07	ROCHAN BANSTOLA	UR	Part-Time	<i>Not Shortlisted</i>
EEE/08	KUMAR SAURAV DEEP	OBC	Full-Time	Provisionally Shortlisted
EEE/09	ANIL KUMAR T R B	SC	Part-Time	Provisionally Shortlisted
EEE/10	UDAY KUMAR	UR	Full-Time	Provisionally Shortlisted
EEE/11	BRAJESH KUMAR YADAVENDU	OBC	Full-Time	Provisionally Shortlisted
EEE/12	RAHUL KUMAR	SC	Full-Time	Provisionally Shortlisted
EEE/13	SANGAM RAJA	OBC	Full-Time	Provisionally Shortlisted
EEE/14	UDARA ASHOK KUMAR	SC	Full-Time	<i>Not Shortlisted</i>
EEE/15	KRISHNENDU MAITRA	UR	Full-Time	Provisionally Shortlisted
EEE/16	SATYABRATA BEHERA	OBC	Full-Time	Provisionally Shortlisted
EEE/17	SATADAL BHOWMIK	UR	Full-Time	Provisionally Shortlisted
EEE/18	NIBEDITA GHOSH	OBC	Full-Time	Provisionally Shortlisted
EEE/19	DEEPAK KUMAR	UR	Full-Time	<i>Not Shortlisted</i>
EEE/20	CHANDAN KUMAR	OBC	Full-Time	Provisionally Shortlisted
EEE/21	NILANJAN DAS	SC	Full-Time	<i>Not Shortlisted</i>



EEE/22	DIBYENDU BHATTACHARJEE	UR	Full-Time	Provisionally Shortlisted
EEE/23	ANURAG KUMAR	OBC	Full-Time	Not Shortlisted
EEE/24	AMRITO DEBNATH	OBC	Full-Time	Provisionally Shortlisted
EEE/25	SUMIT KUMAR	OBC	Full-Time	Not Shortlisted
EEE/26	MANENDRA PATWA	SC	Full-Time	Provisionally Shortlisted
EEE/27	RAVI RANJAN KUMAR	OBC	Full-Time	Provisionally Shortlisted

Instructions

1. The short-listed candidates in the first round of Ph. D. admission process are to be present physically in the Department of electrical and Electronics Engineering, National Institute of Technology Sikkim for the next phase of admission process.
2. The date and time for reporting: **28.05.2022 at 9:00 am.**
3. At the time of reporting to the department, you have to produce the following documents in original:
 - i. Proof of Date of Birth (10th class Board Examination admit card).
 - ii. 10th class Board Examination marksheet and certificate.
 - iii. 12th class Board Examination marksheet and certificate or Equivalent.
 - iv. B. Tech Degree Certificate and Marksheet of all semester.
 - v. M. Tech Degree Certificate and Marksheet of all semester. For M. Tech. pursuing candidates, s/he has to submit her/ his marksheet up to 3rd semester and one document from Head of the Department stating that s/he has appeared in final semester and tentative publish date of result.
 - vi. Reservation/ category certificate, as applicable.
4. In this phase of selection, the candidates have to appear one off-line test followed by an interview.
5. During the selection process, if it is found that any candidate submitted any incorrect/ false statement, her/ his candidature will be cancelled immediately.
6. During the selection process, if any candidate is found using any unfair means, her/ his candidature will be cancelled immediately.


15/5/22
Head of the Department

Dept. of Electrical & Electronics Engineering
National Institute of Technology Sikkim
Ravangla, South Sikkim-737139