Advertisement No.: NITSK/RECT./Visvesvaraya-Scheme/PhD/2024-25/129 Date: 8th Aug, 2024

Applications are invited for admission to full-time Ph.D programs in the department of Electronics and Communication Engineering for two seats under Visvesvaraya Scheme supported by the Ministry of Electronics and Information Technology (MEITY), Government of India. Following are the research areas:

VLSI Design/ Embedded System/ Speech Processing

Last date of online application: 26th August, 2024.

Minimum Eligibility Criterion

- 1. In Engineering Discipline: BE/B.Tech and ME/M.Tech in relevant branches of Engineering/Technology with minimum 60% marks or 6.5 CGPA in both. In exceptional cases, brilliant students from CFTIs with CGPA of more than 9.0 (85% marks) in B.E./B.Tech. may be recommended by the concerned department as per the institute rules.
- 2. For SC/ST/PwD candidates, a relaxation of 5% marks or 0.5 CGPA will be considered, i.e., minimum of 55% marks or 6.0 on the 10 point scale.
- 3. The fellowship/scholarship will be awarded as per the directions of Visvesvaraya Scheme supported by the Ministry of Electronics and Information Technology (MEITY), Government of India.

Fellowship: Rs. 38,750 per month in 1st and 2nd year and @Rs. 43,750 per month in 3rd, 4th and 5th years of PhD (support till PhD completion or 5 years whichever is earlier). *Research Contingency Grant Support:* An amount of Rs. 1,20,000/Year/Full-time PhD candidate for support duration of PhD candidate.

4. The final selection process to Ph.D. programme at NIT Sikkim will be through written test and/or interview. The decisions of the institute in this regard are final and no queries in this regard shall be entertained.

Fill the google form for the application with following link:

Application for Ph.D. Admission under Visvesvaraya PhD scheme - Google Forms

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