
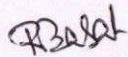


NATIONAL INSTITUTE OF TECHNOLOGY SIKKIM										SGPA
M. Tech Microelectronics and VLSI Design										
Year: 2016			Semester: 1st			Session: 2016-2017				
Mid/ End Semester			Subject Marks/ Grade							
Roll No	Name of the Student	Credit 3 EC 5001	Credit 3 EC 5002	Credit 3 EC 5003	Credit 3 EC 5004	Credit 3 EC 5005	Credit 2 EC 5090	Credit 2 EC 5091		
1	M160001EC POOJA KUMARI	B	C	C	S	B	A	B	8.11	
2	M160006EC TAPAS KUMAR OJHA	D	C	C	C	B	B	B	7.21	
3	M160021EC VIRENDRA PAL KOSALE	B	A	B	B	A	B	A	8.42	
4	M160013EC RUDRA PRAKASH BANERJEE	A	S	A	S	B	A	A	9.16	
5	M160002EC SWETANSHU KUMAR BIPUL	A	A	A	A	B	S	A	8.95	
6	M160016EC AMIT KUMAR SINGH	C	A	B	A	B	A	A	8.37	
7	M160010EC SANGITA TIARI	A	S	A	S	A	A	A	9.32	
8	M160019EC MANJEET KUMAR	A	S	A	S	A	A	A	9.32	
9	M160020EC PANDILLA SHIVAKUMAR	B	A	B	A	C	A	A	8.37	
10	M160004EC CHANDRA PRAKASH SINGH	B	S	B	A	B	A	A	8.68	
11	M160017EC MONALISA B KUJUR	D	D	C	D	D	B	B	6.58	

EC5001: Introduction to VLSI Design, EC 5002: Semiconductor Device Theory and Modelling, EC 5003: VLSI Technology and Processing, EC 5004: Analog Integrated Circuit Design, EC: 5005 Modelling of Digital System, EC 5090: Semiconductor device simulation and process modeling Lab, EC 5091: Modeling of digital system Lab

  
Controller In-Charge (Examination)

  
Dean In-Charge (Academic Affairs)