

Electronics & Communication Engineering, 1st Sem (M.Tech)

	8:30-9:30	9:45-10:45	10:45-11:45	11:45-12:45	12:45-2:00	2:00-3:00	3:00-4:00	4:00-5:00	5:00-6:00
Mon		EC5004 (SKJ) (DCR)	EC5001 (PG) (DCR)		B R E A K	EC5005 (PG) (H4)			
Tues		EC5003 (SKJ) (DCR)		EC5002 (WA) (DCR)		EC5091 (PG) (DSP LAB)			
Wed	EC5002 (WA) (DCR)	EC5003 (SKJ) (DCR)	EC5001 (PG) (DCR)			EC5004 (SKJ) (DCR)		EC5005 (PG) (DCR)	
Thurs		EC5003 (SKJ) (DCR)	EC5001 (PG) (DCR)			EC5090 (SKJ)			
Fri		EC5005 (PG) (DCR)	EC5004 (SKJ) (H3)			EC5002 (WA) (DCR)			

EC5001 Basic of VLSI, **EC5002** Semiconductor device theory and Modelling, **EC5003** Semiconductor Material Technology, **EC5004** Analog Integrated Circuit Design, **EC5005** Modelling of Digital System, **EC5090** Micro Electronics Design Lab-2, **EC5091** Modelling of digital system lab.

SJK- Dr. Sanjay Kumar Jana, **PG-**Mrs. Priti Gupta, **WA-**Dr. Waquar Ahmed,