

An Instruction Enhancement Programme (IEP) on "Mixed Signal and RFIC design " **will be held at NIT Sikkim (PI) from 5<sup>th</sup> to 9<sup>th</sup> June, 2017** with the assistance of IIT Kharagpur (RC) under Special Manpower Development Programme for Chips to System Design (SMDP-C2SD), sponsored by MeitY, Govt. of India.

In this regard, CIs/Co-CIs/faculty members under SMDP-C2SD are requested to kindly fill/ submit the IEP Registration Form for participation in IEP on SMDP-C2SD website ([http://www.smdp2vlsi.gov.in/smdp2vlsi/c2s/login\\_c2s.jsp](http://www.smdp2vlsi.gov.in/smdp2vlsi/c2s/login_c2s.jsp)) under your respective login.